	Utech
Name :	
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Invigilator's Signature :	

CS/M.Tech(CST)/SEM-1/CST-1101/2012-13 2012

ADVANCED OPERATING SYSTEMS

Time Allotted: 3 Hours Full Marks: 70

The figures in the margin indicate full marks.

Candidates are required to give their answers in their own words as far as practicable.

GROUP - A

(Multiple Choice Type Questions)

- 1. Choose the correct alternatives for the following: $10 \times 1 = 10$
 - i) Election algorithm is used for
 - a) Deadlock detection
 - b) Deadlock prevention
 - c) Deadlock avoidance
 - d) None of these.
 - ii) Shell is the exclusive feature of
 - a) UNIX

- b) DOS
- c) System software
- d) Application software.

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iii)	A s	scheduler which selects	s pro	ocesses from secondary
	stor	rage device is called		A Paringer (y' Exercisings 3 and Excission)
	a)	Short term scheduler		
	b)	Long term scheduler		
	c)	Medium term schedule	er	
	d)	Process scheduler.		
iv)	Whi	ich of the following a	appro	oaches do not require
	kno	wledge of the system sta	ate?	
	a)	Deadlock detection	b)	Deadlock prevention
	c)	Deadlock avoidance	d)	None of these.
v)	Whi	ich among the followir	ng is	not an advantage of
	Dis	tributed systems?		
	a)	Reliability	b)	Incremental growth
	c)	Resource sharing	d)	none of these.
vi)	Asy	mmetric Key Cryptogr	aphy	is also called
	Cry	ptography.		
	a)	Secret Key	b)	Private Key

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d) Both (a) and (b).

c) Public Key



vii)	The	key size DES algor	rithm is	GICIAN	
	a)	56 bits	b)	64 bits	7
	c)	72 bits	d)	80 bits.	
viii)	Com	amit protocols enfo	rce		
	a)	Conditional guara	antee		
	b)	Global atomicity			
	c)	Autonomous reas	signment		
	d)	All of these.			
ix)	Stat	ic voting scheme is	s proposed	by	
	a)	Dasser	b)	Barbara et al	
	c)	Hydra	d)	Gifford.	
x)	GSM	I is an example	of	generation cellula	ar
	netv	vorks.			
	a)	First	b)	Second	
	c)	Third	d)	Fourth.	
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(Short Answer Type Questions)

Answer any three of the following.

 $3 \times 5 = 15$

- 2. What is event ordering ? How the logical clock is implemented for that ? 2+3
- 3. Prove that presence of a cycle in general resource allocation graph is a necessary but not a sufficient condition for the existence of deadlock.

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- 4. In RSA, Let P = 7, Q = 17 and Cipher Text (CT) = 15. Calculate Encryption key (E), Decryption key (D) and Plain Text (PT).
- 5. How authentication and confidentiality are ensured by 2-key Digital Signature?
- 6. Write down the desirable features of a good message passing system. 5

GROUP - C

(Long Answer Type Questions)

Answer any *three* of the following. $3 \times 15 = 45$

- 7. a) What is an idempotent operation ? Which of the following operations are idempotent and why ?
 - (i) read next record (filename)
 - (ii) read record (filename, record no)
 - (iii) append record (filename, record)
 - (iv) write record (filename, after record no, record)
 - (v) seek (filename, position)
 - (vi) increment (variable name).

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- b) Which of the following statements concerning the total ordering relation ⇒ are true? Briefly justify your answer.
 - (i) An event a that happened before event b in physical time will always satisfy $a \Rightarrow b$.
 - (ii) If each process increments its logical clock by a different number, the total ordering relation \Rightarrow will not hold.
 - (iii) If the delay of message transfer varies from time to time, the total ordering relation \Rightarrow will not hold. 6
- c) In IPC by message passing define the header of each message.
- 8. a) What is stub? How stub is generated?
 - b) What are the main issues to design a transparent RPC model?
 - c) A server is designed to perform simple integer arithmetic operations: addition, subtraction multiplication and division. Client interacts with the server by using an RPC mechanism. In case of an error such as division by zero or arithmetic overflow, the server must suitably inform the client about the type of error. Describe the content of the call and reply the messages of this RPC application. Explain the purpose of each component. 6

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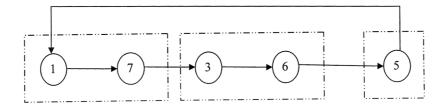
d)	Differentiate between stateless and state full server.
	Why some distributed application uses stateless server
	in spite of the fact that state full server provide more
	efficiency than stateless server?

- 9. a) Design a finite state automata illustrating 3-phase commit protocol.
 - b) Consider a system with three sites employing two-phase commit protocols. Illustrate a situation where in a site may not be able to arrive at a consistent decision concerning the outcome of the transaction in the event of site failures. Assume that a site can communicate with any other operating site to check the outcome of transaction.
- 10. a) What are the approaches are taken for improving the efficiency of access matrix?
 - b) What are the advantages and disadvantages of capabilities?
 - c) Explain the state transitions and restrictions inBel LaPadula model.5
- 11. a) Why is detection of deadlock in distributed system more involved than in a centralized system ? What is a pantom deadlock ? 3+2

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b) Describe the distributed deadlock detection algorithm and so how it can be applied to the following system to detect the deadlock.



12. Write short notes on any *three* of the following :

 3×5

- a) Bully Algorithm
- b) Difference between network system and distributed system.
- c) Necessary and desirable properties of Mutual exclusion algorithm.
- d) Atomic action
- e) Embedded System.

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